

Amendments To The Claims:

The text of all pending claims, (including withdrawn claims) is set forth below. Cancelled and not entered claims are indicated with claim number and status only. The claims as listed below show added text with underlining and deleted text with ~~striketrough~~. The status of each claim is indicated with one of (original), (currently amended), (cancelled), (withdrawn), (new), (previously presented), or (not entered).

Applicants reserve the right to pursue any cancelled claims at a later date.

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (previously presented) A method for coating a substrate having at least one hole, comprising:
  - covering the at least one hole with a plug;
  - applying at least one layer to a surface of the substrate via a low-temperature coating process; and
  - irradiating a near-surface region of the coating layer to improve adhesion of the coating layer to the substrate, and to ensure homogenization of the coating layer without homogenizing a region of the substrate located laterally adjacent the homogenized region.
2. (previously presented) A method according to Claim 1, wherein the substrate is a turbine blade.
3. (cancelled).
4. (previously presented) A method according to Claim 1, wherein the low-temperature coating process is an electrochemical coating method.
5. (previously presented) A method according to Claim 1, wherein the temperature for the low-temperature coating process is below 250°C.

6. (previously presented) A method according to Claim 1, wherein irradiation of the near-surface region is performed using pulsed electron irradiation.

7. (previously presented) A method according to Claim 1, wherein irradiation of the near-surface region is performed using a laser treatment.

8. (previously presented) A method according to Claim 1, wherein during or at the end of irradiation of the near-surface region, the plug is removed from the near-surface region of the hole.

9. (previously presented) A method according to Claim 8, wherein the plug is removed by evaporation.

10. (previously presented) A method according to Claim 1, wherein the coating layer is a ceramic heat insulating layer, or an MCrAl<sub>y</sub> coating where M is selected from the group consisting of iron, cobalt and nickel.

11. (previously presented) A method according to Claim 1, wherein the hole, of which there is at least one, is a film cooling hole or an impingement cooling hole.

12. (previously presented) A method according to Claim 1, wherein the plug is a wax material.

13. (currently amended) A method for coating a turbine component having at least one hole, comprising:

covering the at least one hole with a plug;

applying at least one layer to a surface of the turbine component via a low-temperature coating process; and

irradiating a near-surface region of the coating layer to improve adhesion of the coating layer to the surface of the component, and to ensure homogenization of the coating layer and

without homogenizing a region of the surface of the turbine component located laterally adjacent the homogenized region.

14. (previously presented) A method for recoating a substrate, which has already been used and having at least one hole, comprising:

covering the at least one hole with a plug;

applying at least one layer to a surface of the substrate, via a low-temperature coating process; and

irradiating a near-surface region of the coating layer to improve adhesion of the coating layer to the substrate, and to ensure homogenization of the coating layer and without homogenizing a region of the substrate located laterally adjacent the homogenized region.

15. (cancelled).

16. (previously presented) The method according to Claim 1, wherein the near-surface region of the coating layer is scanned over a period of time by the irradiation.

17. (previously presented) The method according to Claim 5, wherein the temperature for the low-temperature coating process is below 100°C.

18. (previously presented) The method according to Claim 17, wherein the temperature for the low-temperature coating process is 50°C.